ABSTRACT

A REPEAT instruction for repeated execution of an associated instruction ($INST_R$). Once a program counter stores the address for the instruction to be repeated, it remains unchanged until the associated instruction ($INST_R$) has been executed the number of times indicated by a COUNT value in a preloaded register, or alternatively, by the REPEAT instruction itself. In this manner, the present invention reduces the number of instruction fetches required to repeatedly execute the associated instruction ($INST_R$). Consequently, there is a significant improvement in the efficiency of the program code execution.

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